L Number	Hits	Search Text	DB	Time stamp
39	67701	differential with amplifier	USPAT;	2004/08/09 16:33
		·	ЕРО, ЈРО	
41	32857	impedance with match\$3	USPAT;	2004/08/09 13:17
			ЕРО; ЛРО	
42	1374501	adjust\$3	USPAT;	2004/08/09 12:52
72	13,1301	aujusus	ЕРО; ЈРО	2004/00/05 12:52
43	256762	dsp or cpu	USPAT;	2004/08/09 12:52
73	250702	asp of cpu	ЕРО; ЛРО	2004/00/07 12:32
44	3909	(impedance with match\$3) same adjust\$3	USPAT;	2004/08/09 12:52
44	3909	(impedance with matches) same adjustes	EPO; JPO	2004/06/09 12.32
A5	58	(differential with amplifier) some ((impedance with match #2) some		2004/08/09 12:52
45	38	(differential with amplifier) same ((impedance with match\$3) same	USPAT;	2004/08/09 12:52
		adjust\$3)	EPO; JPO	2004/00/00 12 52
46	6	(dsp or cpu) and ((differential with amplifier) same ((impedance with	USPAT;	2004/08/09 12:52
		match\$3) same adjust\$3))	ЕРО; ЈРО	
47	8	("3395358" "3440554" "5596610" "5936896" "6005438"	USPAT	2004/08/09 13:14
		"6028479" "6087899" "6304141").PN.		
48	9488	adjust\$3 with impedance	USPAT;	2004/08/09 15:52
			ЕРО; ЈРО	
49	244	(differential with amplifier) same (adjust\$3 with impedance)	USPAT;	2004/08/09 13:54
			ЕРО; ЈРО	
50	14	(dsp or cpu) and ((differential with amplifier) same (adjust\$3 with	USPAT;	2004/08/09 13:44
		impedance))	ЕРО; ЛО	
51	6	["4490634" "4701694" "4897555" "5530399" "5815103"	USPAT	2004/08/09 13:18
-		"6157332").PN.		
52	9441	active with terminat\$5	USPAT;	2004/08/09 13:45
·-	, , , ,		ЕРО; ЛРО	200 1100/07 15.15
53	12037	back with match\$3	USPAT;	2004/08/09 13:45
),	12037	oack with matches	ЕРО; ЛРО	2004/06/09 13.43
54	9	(active with terminat(5) some (heal; with metal(2))	USPAT;	2004/08/09 13:45
34		(active with terminat\$5) same (back with match\$3)		2004/08/09 13:43
		(4) 00	ЕРО; ЈРО	2004/00/00 12 50
55	2	(differential with amplifier) and ((active with terminat\$5) same (back	USPAT;	2004/08/09 13:50
		with match\$3))	EPO; JPO	
57	2	(active with terminat\$5) same ((differential with amplifier) same	USPAT;	2004/08/09 13:51
		(adjust\$3 with impedance))	ЕРО; ЈРО	
58	1	((differential with amplifier) same (adjust\$3 with impedance)) and snr	USPAT;	2004/08/09 13:51
			ЕРО; ЈРО	
59	347934	capacitor	USPAT;	2004/08/09 13:54
			ЕРО; ЈРО	
60	38	((differential with amplifier) same (adjust\$3 with impedance)) same	USPAT;	2004/08/09 13:56
		capacitor	EPO; JPO	
61	137870	"a/d" or "d/a"	USPAT;	2004/08/09 13:56
			ЕРО; ЈРО	
62	34	((differential with amplifier) same (adjust\$3 with impedance)) and	USPAT;	2004/08/09 14:11
		("a/d" or "d/a")	ЕРО; ЛРО	
63	8286	plurality with impedance	USPAT;	2004/08/09 14:11
		[*	ЕРО; ЛРО	
64	1361933	switch\$3	USPAT;	2004/08/09 14:11
- '	1551755		ЕРО; ЛРО	200 1100/07 14.11
65	1536	(plurality with impedance) same switch\$3	USPAT;	2004/08/09 14:12
~~	0,00	(Processor) some switching	ЕРО; ЛРО	2007/00/07 14.12
66	32	(differential with amplifier) same ((plurality with impedance) same	USPAT;	2004/08/09 14:12
υυ	32	(differential with amplifier) same ((plurality with impedance) same switch\$3)		2004/06/09 14:12
67	22		ЕРО; ЛРО	2004/09/00 14:05
67	22	adjust\$3 and ((differential with amplifier) same ((plurality with	USPAT;	2004/08/09 14:25
		impedance) same switch\$3))	ЕРО; ЛРО	2004/00/00 14 55
68	1	6580286.pn.	USPAT;	2004/08/09 14:28
			ЕРО; ЈРО	
69	0	adjust\$3 and 6580286.pn.	USPAT;	2004/08/09 14:28
l			ЕРО; ЈРО	
70	51107	input\$3 with impedance	USPAT;	2004/08/09 15:30
l			ЕРО; ЈРО	
- .	3862	(input\$3 with impedance) same (differential with amplifier)	USPAT;	2004/08/09 15:16
71	3002	[(

72	108	(adjust\$3 with impedance) same ((input\$3 with impedance) same	USPAT;	2004/08/09 15:16
		(differential with amplifier))	ЕРО; ЈРО	
73	1879	adjust\$3 with input with impedance	USPAT;	2004/08/09 15:31
			ЕРО; ЈРО	
75	0	(dsp or cpu) same ((differential with amplifier) same (adjust\$3 with	USPAT;	2004/08/09 15:30
		input with impedance))	ЕРО; ЈРО	
74	88	(differential with amplifier) same (adjust\$3 with input with impedance)	USPAT;	2004/08/09 15:30
			ЕРО; ЈРО	
76	246	adjust\$3 adj2 input adj2 impedance	USPAT;	2004/08/09 15:31
			ЕРО; ЈРО	
77	39	(differential with amplifier) and (adjust\$3 adj2 input adj2 impedance)	USPAT;	2004/08/09 15:32
			ЕРО; ЈРО	
78	9836	(adjust\$3 or contro\$5) with (input with impedance)	USPAT;	2004/08/09 15:52
			ЕРО; ЛРО	
79	9946	(adjust\$3 or control\$5) with (input with impedance)	USPAT;	2004/08/09 15:52
			ЕРО; ЛРО	
80	560	((adjust\$3 or control\$5) with (input with impedance)) same (differential	USPAT;	2004/08/09 15:53
		with amplifier)	ЕРО; ЉО	
81	56966	differential adj2 amplifier	USPAT;	2004/08/09 15:53
			ЕРО; ЈРО	
82	434	((adjust\$3 or control\$5) with (input with impedance)) same (differential	USPAT;	2004/08/09 15:54
		adj2 amplifier)	ЕРО; ЛРО	
83	89	(((adjust\$3 or control\$5) with (input with impedance)) same	USPAT;	2004/08/09 15:55
		(differential adj2 amplifier)) same capacitor	ЕРО; ЛРО	
84	23650	tip with ring	USPAT;	2004/08/09 15:55
			ЕРО; ЛРО	
85	1	((((adjust\$3 or control\$5) with (input with impedance)) same	USPAT;	2004/08/09 15:55
	45	(differential adj2 amplifier)) same capacitor) same (tip with ring)	ЕРО; ЛРО	
86	47	(differential with amplifier) same (back with match\$3)	USPAT;	2004/08/09 16:33
0.7	_		EPO; JPO	2004/00/00 16 20
87	4	(adjust\$3 with input with impedance) and ((differential with amplifier)	USPAT;	2004/08/09 16:39
00	1720	same (back with match\$3))	EPO; JPO	2004/09/00 16:40
88	1720	(adjust\$3 or chang\$3 or control\$5) near2 input near2 impedance	USPAT;	2004/08/09 16:40
00	64	(disc	ЕРО; ЛРО	2004/00/00 16:42
89	04	(differential with amplifier) same ((adjust\$3 or chang\$3 or control\$5)	USPAT;	2004/08/09 16:42
90	3	near2 input near2 impedance)	ЕРО; ЈРО	2004/09/00 16:42
30	3	(tip with ring) and ((differential with amplifier) same ((adjust\$3 or chang\$3 or control\$5) near2 input near2 impedance))	USPAT;	2004/08/09 16:42
91	51918	switch\$3 same (dsp or cpu)	EPO; JPO USPAT;	2004/08/09 16:43
71	21319	switches settle (ash of cha)	EPO; JPO	2004/08/09 10:43
93	1	((differential with amplifier) same ((adjust\$3 or chang\$3 or control\$5)	USPAT;	2004/08/09 16:43
'3	1	near2 input near2 impedance)) and (dsp or cpu)	ЕРО, ЛО	2004/06/09 10:43
		mean 2 mpar near 2 mipedance)) and (usp of cpu)	EFO, JPO	l